

EXAMINATION BRANCH
TIME: 10.00 AM TO 12.00 NOON
No: SRU/Ex.B/2023/
TIME-TABLE
I YEAR M.TECH. I SEMESTER (R23 REGULATION – 2023 BATCH) REGULAR EXAMINATIONS, DECEMBER– 2023/JANUARY-2024

DATE & DAY	CONSTRUCTION TECHNOLOGY AND MANAGEMENT	POWER ELECTRONICS	EMBEDDED SYSTEMS	VLSI	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING
28-12-2023 THURSDAY	QUANTITATIVE TECHNIQUES IN PROJECT MANAGEMENT	ANALYSIS OF POWER ELECTRONIC CONVERTERS	EMBEDDED SYSTEMS DESIGN	CMOS VLSI DESIGN	ADDITIVE MANUFACTURING PROCESSES	ADVANCED DATA STRUCTURES
29-12-2023 FRIDAY	ADVANCED CONSTRUCTION MATERIALS AND TESTING	POWER ELECTRONIC CONTROL OF DC DRIVES	DIGITAL SYSTEM DESIGN	ANALOG INTEGRATED CIRCUIT DESIGN	COMPUTER AIDED DESIGN MODELING	COMPUTABILITY, COMPLEXITY AND ALGORITHMS
02-01-2024 TUESDAY	CONSTRUCTION TOOLS AND EQUIPMENTS	HVDC TRANSMISSION	FPGAS DESIGN	VLSI TESTING & TESTABILITY	COMPUTER INTEGRATED MANUFACTURING	HIGH PERFORMANCE COMPUTING PRINCIPLES AND PRACTICES
03-01-2024 WEDNESDAY	RESOURCE MANAGEMENT AND CONTROL IN CONSTRUCTION	DYNAMICS OF ELECTRICAL MACHINES	SYSTEM ON CHIP ARCHITECTURE	HIGH SPEED DIGITAL DESIGN	MATERIALS AND ENERGY SOURCES FOR ADDITIVE MANUFACTURING	IMAGE AND VIDEO PROCESSING
04-01-2024 THURSDAY	BUILDING PLANNING AND BYE-LAWS	DISTRIBUTED GENERATION & MICRO GRID	WIRELESS SENSOR NETWORKS	LOW POWER VLSI	FINITE ELEMENTS ANALYSIS	--
05-01-2024 FRIDAY	RESEARCH METHODOLOGY AND IPR	RESEARCH METHODOLOGY AND IPR	RESEARCH METHODOLOGY AND IPR	RESEARCH METHODOLOGY AND IPR	RESEARCH METHODOLOGY AND IPR	--

NOTE: ANY CLASHES / OMISSIONS IN THIS TIME-TABLE MAY BE BROUGHT TO THE NOTICE OF THE UNDERSIGNED.


CONTROLLER OF EXAMINATIONS
Dt.: 21.12.2023