



EXAMINATION BRANCH

TIME-TABLE

TIME: 10.00 AM TO 01.00 PM

M.TECH. I SEMESTER (RA18 REGULATION – 2018 & 2019 BATCHES) SUPPLEMENTARY EXAMINATIONS, MAY- 2024

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING
14-05-2024 TUESDAY	Special Machines	Digital System Design	Digital System Design	Computer Integrated Manufacturing	Advanced Data Structures
16-05-2024 THURSDAY	Analysis of Power Electronic Converters	RTL Simulation and Synthesis with PLDs	RTL Simulation and Synthesis with PLDs	Advanced Computer Aided Design & Manufacturing	Machine Learning
20-05-2024 MONDAY	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR
22-05-2024 WEDNESDAY	Power Electronic Control of DC Drives	--	Analog Integrated Circuit Design	Advanced Material Technology	Distributed Systems
24-05-2024 FRIDAY	--	Embedded C	Embedded C	--	Software Process & Project Management
28-05-2024 TUESDAY	HVDC Transmission	ARM Architectures	--	Special Manufacturing Process	--

NOTE: ANY CLASHES / OMISSIONS IN THIS TIME-TABLE MAY BE BROUGHT TO THE NOTICE OF THE UNDERSIGNED.

CONTROLLER OF EXAMINATIONS

Dt.: 23.04.2024

EXAMINATION BRANCH

TIME-TABLE

TIME: 10.00 AM TO 01.00 PM

M.TECH. II SEMESTER (RA 18 REGULATION – 2018 & 2019 BATCHES) SUPPLEMENTARY EXAMINATIONS, MAY- 2024

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING
15-05-2024 WEDNESDAY	Power Electronic Control of AC Drives	Embedded OS & RTOS	CMOS VLSI Design	Manufacturing Systems Simulation	Advanced Algorithms
17-05-2024 FRIDAY	Power Electronic Applications to Renewable Energy	--	ASIC and SOC	Theory of Metal Cutting and Forming	Big Data Analytics
21-05-2024 TUESDAY	Microprocessor and Microcontroller	Advanced Digital Signal Processing	Advanced Digital Signal Processing	Advanced Tool Design	Deep Learning
23-05-2024 THURSDAY	--	Embedded Networking	Embedded Networking	NDE & NDT	--
		High Speed Digital Design	High Speed Digital Design		
27-05-2024 MONDAY	Power Quality	System On Chip Architecture	--	--	Cryptography and Network Security
	Dynamics of Electrical Machines				

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CONTROLLER OF EXAMINATIONS

Dt.: 23.04.2024

EXAMINATION BRANCH

TIME-TABLE

II YEAR M.TECH. I SEMESTER (RA18 REGULATION – 2018, 2019 BATCHES) SUPPLEMENTARY EXAMINATIONS, MAY - 2024

TIME: 02.00 PM TO 05.00 PM

Date & Day	SUBJECTS (RA18 – 2018, 2019 Admitted Batches)				
	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	COMPUTER SCIENCE & ENGINEERING	ADVANCED MANUFACTURING SYSTEMS
14-05-2024 TUESDAY	FLEXIBLE AC TRANSMISSION SYSTEMS (FACTS)	DESIGN FOR TESTABILITY	DESIGN FOR TESTABILITY	NATURAL LANGUAGE PROCESSING	MANUFACTURING POLYMERS
16-05-2024 THURSDAY	OPERATIONS RESEARCH	OPERATIONS RESEARCH	OPERATIONS RESEARCH	CYBER LAWS	INDUSTRIAL SAFETY
	INDUSTRIAL SAFETY	INDUSTRIAL SAFETY	INDUSTRIAL SAFETY		

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CONTROLLER OF EXAMINATIONS

Dt.: 23.04.2024