SR UNIVERSITY

Department of
Electronics and Communication Engineering
School of Engineering

3rd BOARD OF STUDIES MEETING
2022-2023

SR UNIVERSITY

Department of Electronics and Communication Engineering
Meeting of Board of Studies

Venue: Online/ Microsoft Teams

Date: 30-05-2022

Time: 11:00 am

AGENDA

BoS 3-1: Confirmation of action report of the 2nd BoS meeting

BoS 3-2: To discuss the feedback analysis of the stakeholders and the action taken as recommended by the DAC.

BoS 3-3: To approve the III & IV year Course Structure and syllabus of B.Tech ECE.

BoS 3-4: Reframing of courses based on R-20 Course structure

BoS 3-5: Award of Honors degree and scheme of the program

BoS 3-6: Offer of Minor program for students of EEE and CSE branches

BoS 3-7: Any other item with teh permission of the Chair

CHAIRMAN, BOS

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

MEMBERS OF BOARD OF STUDIES

Academic Year 2022 - 2023

Date: 30-05-2022   **Venue: Microsoft Teams**

Members Present

Sl.No.	Name of the Member	Position held	Signature
1.	**Dr. Sandip Bhattacharya** Assoc.Prof.& Head (ECE) SR University,Warangal	Chairman	
2.	**Dr. D. Sriram Kumar** Professor, NIT, Trichy.	External Subject Expert	
3.	**Dr. Govinda Raj.** Senior Principal Scientist CEERI Centre, CSIR Madras complex	Industry Representative	
4.	**Prof.Vijaya Gunturu** Dean School of Engg. SR University	Member	
5.	**Dr. Syed Musthak Ahmed** Professor, SR University	Member	
6.	**Dr. Usha Desai,** Professor / ECE SR University	Member	
7.	**Dr. K. Raj kumar** Associate Professor, SR University	Member	
8.	**Dr. J. Ravichander** Associate Professor, SR University	Member	

9.	**Dr. Leo Joseph** Assistant Professor SR University	Member
10.	**Dr. Subham Tayal** Assistant Professor SR University	Member

Members Not Present

Sl.No.	Name of the Member	Position held
1.	**Dr. J.Tarun Kumar** Professor, SR University	Member

Department of Electronics and Communication Engineering

Meeting of Board of Studies

Venue: Online/ Microsoft Teams

Date: 30-05-2022

Time: 11.00 am

AGENDA & NOTES

BoS 3-1: Confirmation of action report of the 2nd BoS meeting

Notes: The minutes of the 2nd BoS were circulated to all the members. The board is requested to approve the same. The minutes are enclosed as Annexure I

Resolution: The board confirmed the minutes of the 2nd BoS meeting

BoS 3-2: To discuss the feedback analysis of the stakeholders and the action taken as recommended by the DAC.

Notes: The feedback analysis of the stakeholders and the recommendations of the DAC have been discussed by the members.

The following are the resolutions taken in the DAC meeting based on the feedback of the stakeholders.

1. Alumni students suggested introducing a course on basic components that would be instrumental in supporting course projects

2. The Employers highlighted the increasing demand for expertise in chip design, emphasizing that students need practical experience with advanced design tools and methodologies, which are essential for roles in semiconductor design and engineering. They also emphasized the growing impact of wireless communication technologies across various industries, highlighting the need to prepare students for future-focused roles in this field.

3. Parents suggested introducing course on real world applications as engineers should be aware of the problems faced by the society

4. Faculty members suggested adding Electronic Design course to the curriculum, that would equip students with a broader understanding

of the field, enhancing their skills and knowledge in electronic design principles and applications.

The feedback analysis of the stakeholders and the recommendations of the DAC are enclosed as Annexure II.

Based on the recommendation of the stakeholders, the following new courses been added to the curriculum w.e.f the academic year 2022-23 and are presented to the BoS for approval. The list of new courses proposed is enclosed as Annexure III.

Resolution: The BoS members discussed the recommendations of the stakeholders and approved the new courses to be added to the curriculum w.e.f 2022-23 as recommended by the DAC.

BoS 3-3: To approve the III & IV year Course Structure and syllabus of B.Tech ECE.

Notes: The course structure and syllabus of B.Tech ECE is drafted as per the UGC guidelines and is presented for the approval of the BoS Members. The proposed Course Structure and Syllabus will be applicable w.e.f academic year 2022-23

Resolution: Approved and recommended to the academic council

BoS 3-4: Reframing of courses based on R-20 Course structure

Notes: ECE III year / II Sem :The course entitled as "Microcontroller for Embedded System" is renamed as "Microcontrollers and Applications (PC)".
Program Elective: The course entitled as "Sensor Technologies" is renamed as "Sensors and Actuators (PE)".

Discussion:

Dr. Sandip Bhattacharya, Head Dept. OF ECE, Chairman BOS has brief about Microcontroller for Embedded System seems specific, and affix with a particular domain .The application and its orientation are limited and bounded to Embedded Systems.
Based on the inputs received from the internal experts and external bodies the subject may be generalized in terms of widening its coverage and applications hence it is suggested to rename the subject as Microcontroller and Applications with refined contents.
The course entitled as "Sensor Technologies" is renamed as   "Sens ors and Actuators (PE)". Based on the inputs received from the internal experts and external industrial bodies the subject may be refined with sensor along with actuator in view of bridging the academic and industrial needs.

Resolution: Approved and recommended to the academic council

BoS 3-5:   Award of Honors degree and scheme of the program

Notes: → Dr. Sandip Bhattacharya, & Head Dept. of ECE, Chairman BoS has briefed about the department offering honours degree for students of B.Tech ECE

Discussion:

→ Dr. Sandip Bhattacharya, Head Dept. of ECE & Chairman BoS has briefe that a student must earn 18 additional credits for award of honors degree. The eligibility criteria for opting for honors degree is proposed as having CGPA>7.5.

→ Dr. Govindaraj raised the query about the criteria for fixing 18 credits for award of honours degree?
→ Dr. Sriramkumar suggested that 178 credits is slightly on the other side.
→Dr. Sandip Bhattacharya addressed that, as per the UGC regulation, 20 credits is suggested for award of honours degree and the university has fixed these credits at 18 for award of honours degree.
→ Dr. Sriram Kumar suggested using MOOC platform for some courses
→ Dr. Sriram Kumar and Dr. Govinda Raj suggested that Honors course should be offered from 5th semester onwards.
→ Dr. Sriram Kumar suggested that there is limited number of options, ascertain the course availability before offering this course.
→ Dr. Sandip Bhattacharya, addressed that Honors program will be offered from 3rd year onwards and courses will be definitely be ascertained with NPTEL and other MOOCS course list.

Resolution: Approved and recommended to the academic council

BoS 3-6:   Offer of Minor program for students of EEE and CSE branches

Notes: → Dr. Sandip Bhattacharya, & Head Dept. of ECE, Chairman BoS has briefed about the department offering minors degree in the following for students of B.Tech EEE and CSE.

1. Minors in Embedded systems
2. Minors in Communication Engineering
3. Minors in VLSI design

Students opting minor program should have CGPA>7.0, the students can opt subjects suitably based on their interest.

The following points were discussed by external BoS members
a) It has been suggested to make sure that the course offered along with their syllabi must not be duplicated with the courses of the department to whom the minor is offered.
b) At least one unit in every syllabus should be application-oriented towards the department to whom the minor is offered.

c) It is suggested that a minor in Mechanical Engineering may be offered in the coming years.

d) The title should be catchy to attract a greater number of students to minor programs.
e) The syllabus must be easier than regular courses in minor program.

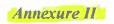**Resolution**: Approved and recommended to the academic council

BoS 3-7: Any other item with teh permission of the Chair
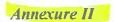
--NIL--

The meeting ended with a vote of thanks by Chairman

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Board of Studies

The inputs of the stakeholders based on their feedback are as follows:

Sl. No	Stakeholder	Inputs from Stakeholders	Resolution
1	Employers	Employers highlighted the increasing demand for expertise in chip design, emphasizing that students need practical experience with advanced design tools and methodologies, which are essential for roles in semiconductor design and engineering.	Based on their suggestion, the courses on Analog circuits, Digital Circuitsare included in the curriculum to emphasize the application of semiconductor IC design techniques for solving complex challenges in chip design and fabrication. These courses will equip students with practical skills in both analog and digital IC design, which are essential for roles in semiconductor design and engineering.
		Employers emphasized the growing impact of wireless communication technologies across various industries, highlighting the need to prepare students for future-focused roles in this field. These courses will foster creativity, innovation, and the development of advanced communication systems and solutions.	Advanced communication technologies are transforming industries, from wireless networks to data transmission. Hence, courses on Principles of communication is proposed to prepare students for future roles by equipping them with the knowledge and skills to develop and optimize cutting-edge communication systems.
2	Parents	Parents suggested introducing course on real world applications as engineers should be aware of the problems faced by the society	Recognizing the significance of the real world applications highlighted by parents, a course on Design for Social Impact is proposed to be added.
3	Alumni	Alumni students are suggested for the introduction of a course on basic components that would be instrumental in supporting course projects for current students. They believe that offering this foundational course would equip students with essential skills and knowledge, enabling them to effectively tackle their projects.	Based on recommendations from alumni, it is proposed to introduce a 3-credit course on Sensors and Transducers to the curriculum. This addition would provide students with the opportunity to incorporate this knowledge into their course projects, enhancing their practical experience and understanding of the subject matter.

| 4 | Faculty | Faculty members have suggested the addition of an Electronic Design course to the curriculum. This elective would equip students with a broader understanding of the field, enhancing their skills and knowledge in electronic design principles and applications. | In response to faculty recommendations, the curriculum will include an elective course on Sustainable Electronics Design. This course aims to provide students with a comprehensive understanding of environmentally friendly design principles and practices within the electronics industry. |

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List of New Courses offered from the academic year 2022-23

S.No.	Course Code	Course	Hours / Week			
			L	T	P/D	C
1	220E116	Analog Circuits	3	-	-	3
2	220E117	Digital Circuits	3	-	-	3
3	220E118	Principles of Communication	3	-	-	3
4	220E119	Sensors and Transducers	3	-	-	3
5	220E120	Sustainable Electronics Design	3	-	-	3
6	220E175	Design for Social Impact	3	-	-	3

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